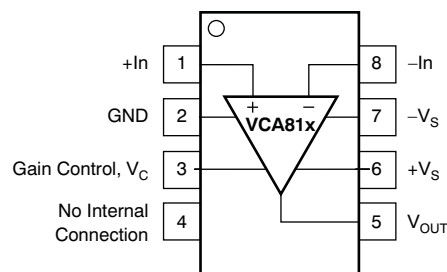


## DEM-VCA-SO-1A Demonstration Fixture

### 1 Description

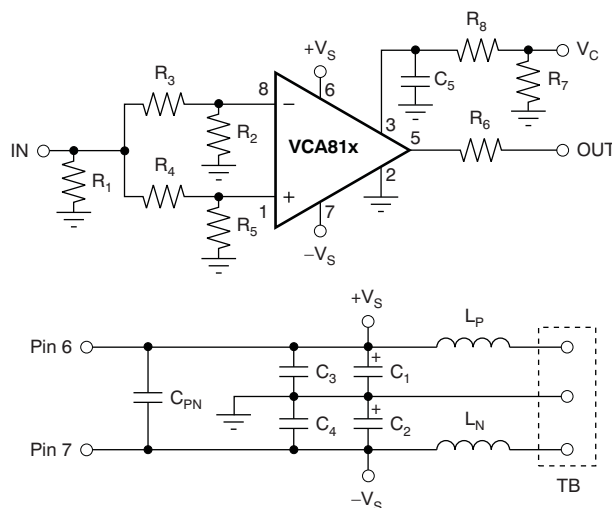
The DEM-VCA-SO-1A demonstration fixture is a generic, unpopulated printed circuit board (PCB) for voltage-controlled amplifiers (VCAs) in SO-8 packages. [Figure 1](#) shows the package pinout for this PCB. For more information on VCAs and good PCB layout techniques, see the individual VCA data sheets available for download at [www.ti.com](http://www.ti.com).



**Figure 1. SO Package Pinout, Top View**

### 2 Circuit

The circuit schematic in [Figure 2](#) shows the connections for all possible components. Each configuration uses only some of the components.



**Figure 2. Schematic for DEM-VCA-SO-1A**

### 3 Components

Components that have RF performance similar to the ones in [Table 1](#) may be substituted.

**Table 1. Component Descriptions**

PART	DESCRIPTION
C <sub>1</sub> , C <sub>2</sub>	Tantalum Chip Capacitor, SMD EIA Size 3528, 20V
C <sub>3</sub> , C <sub>4</sub> , C <sub>PN</sub>	Multilayer Ceramic Chip Capacitor, SMD 1206, 50V
IN, OUT, V <sub>C</sub>	SMA or SMB Board Jack (Amphenol 901-144-8)
L <sub>P</sub> , L <sub>N</sub>	EMI-Suppression Ferrite Chip, SMD 1206 (Steward LI 1206 B 900 R)
TB	Terminal Block, 3.5mm Centers (On-Shore Technology ED555/3DS)
R <sub>X</sub>	Metal Film Chip Resistor, SMD 1206, 1/8W

Please refer to [Figure 3](#) for the location of the following components:

- R<sub>1</sub> is the input resistance matching the source impedance.
- Use either R<sub>3</sub> or R<sub>4</sub> to select the inverting or noninverting input of the VCA81x.
- R<sub>2</sub> or R<sub>5</sub> allow the user to connect the unused input to ground.
- R<sub>6</sub> is the output resistor.
- R<sub>7</sub> provides matching impedance to the source driving the control voltage pin V<sub>C</sub>.
- R<sub>8</sub> and C<sub>5</sub> are added to provide low-pass filtering, if required.
- L<sub>P</sub> and L<sub>N</sub> are ferrite chips that can reduce interactions with the power supply at high frequencies; if not desired, they can be replaced with 0Ω resistors.

The power supplies are each bypassed with two capacitors:

- C<sub>1</sub> and C<sub>3</sub>, respectively, for the positive supply.
- C<sub>2</sub> and C<sub>4</sub> for the negative supply.
- C<sub>1</sub> and C<sub>2</sub> are usually set between 2.2μF and 6.8μF, whereas C<sub>3</sub> and C<sub>4</sub> are 0.1μF ceramic capacitors.
- C<sub>PN</sub>, usually set at 10,000pF, is connected between the positive and negative power supplies.

## 4 Board Layout

This demonstration fixture is a four-layer PCB. It uses both a ground plane and power traces on the inner layers. The ground plane has been opened up around op amp pins that are sensitive to capacitive loading. Power-supply traces are laid out to keep current loop areas to a minimum. The SMA (or SMB) connectors may be mounted either vertically or horizontally onto the board edge. The location and type of capacitors used for power-supply bypassing are crucial for high-frequency amplifiers. The tantalum capacitors,  $C_1$  and  $C_2$ , do not need to be close to pins 6 and 7 on the PCB and may be shared with other amplifiers. See the individual VCA data sheet for more information on proper board layout techniques and component selection.

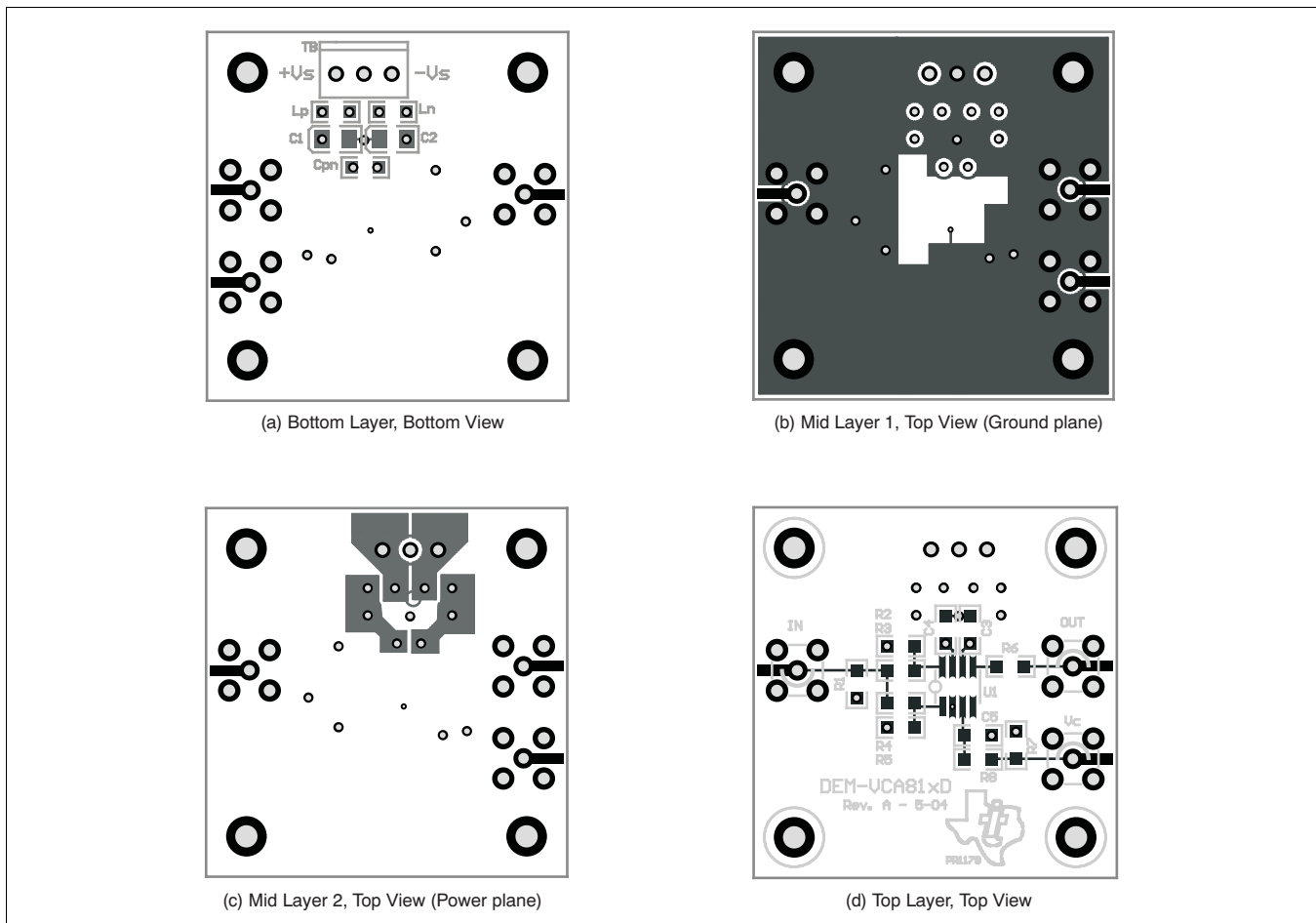


Figure 3. DEM-VCA-SO-1A Demonstration Board Layout

## 5 Measurement Tips

This demonstration fixture, with the component values shown, is designed to operate in a  $50\Omega$  environment; most data sheet plots are obtained in this manner. It is easy to change the component values for different input and output impedance levels. However, do not use high-impedance probes; they represent a heavy capacitive load to the VCAs, and will alter the device response. Instead, use low-impedance ( $\leq 500\Omega$ ) probes with adequate bandwidth. The probe input capacitance and resistance set an upper limit on the measurement bandwidth. If a high-impedance probe must be used, place a  $100\Omega$  resistor on the probe tip to isolate its capacitance from the circuit.

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